

**BHARATI VIDYAPEETH DEEMED UNIVERSITY  
COLLEGE OF ENGINEERING, PUNE-43  
DEPARTMENT OF E & TC ENGINEERING**

**TIME TABLE 2011 – 12**

**CLASS: B. Tech (E & TC) Sem: III  
G.F.M: Mrs. A. B. Patil**

**w. e. f – /07/2011  
Room No.: C-201**

TIME	MON	TUE	WED	THURS	FRI	TIME	SAT
10:30	ED	E1-ED	E1-DSF	E1-FIC	E1-NA	08:30	ED
11:30	NA	E2-DSF E3-NA	E2-NA E3-FIC	E2-ED E3-DSF	E2-FIC E3-ED	09:30	EM-III
12:30	RECESS					10:30	
01:15	EM-III	EM-III	NA	NA	FIC	11:30	DSF
02:15	FIC	FIC	DSF	ED	DSF	12:30	DSF
03:15	SHORT RECESS						
03:30	ED				EM-III		
04:30	NA				FIC		

**DSF -W-508; EDC-W-406(A); NA-W-406(B); FIC-W-301**

SUBJECT	STAFF MEMBER		
	Theory	Practical	
Electronic Devices	Mr. Sudhir Bussa	E1 E2 E3	Mr. Sudhir Bussa
Network Analysis	Mrs. A.B. Patil	E1 E2 E3	Mrs. A.B. Patil
Fundamentals of Instrumentation & Contro	Yadnesh Behre(V)	E1 E2 E3	Ms. P. D. Kulkarni
Data Structures & Files	Mr.Subhash Ohal (V)	E1 E2 E3	.Subhash Ohal (V)
Engineering Mathematics - III	Engg. Science		

**T.T. Incharge  
(Mr. V.H. Patil)**

**H.O.D**

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**TIME TABLE 2011 – 12**

**CLASS: B. Tech (E & TC) Sem: V  
G.F.M: Mr. P.D. Kadam**

**w. e. f – /07/2011  
Room No.: C-208**

TIME	MON	TUE	WED	THURS	FRI	TIME	SAT
10:30	E1- MPMC	ECD	AVE	ECD	ECD	08:30	EME
11:30	E2-ECD E3-DC	AVE	ECD	DC	EME	09:30	EME
12:30	<b>RECESS</b>					10:30	
01:15	DC	DC	DC	AVE	AVE	11:30	
02:15	MPMC	MPMC	MPMC	MPMC	EME	12:30	
03:15	<b>SHORT RECESS</b>						
03:30		E1-EME	E1-ECD	E1-DC			
04:30		E2-DC E3- MPMC	E2- MPMC E3-EME	E2-EME E3-ECD			

**MPMC-W-407 Comp lab-2; DC-W-510, ECD-W- ; W-509-EME**

SUBJECT	STAFF MEMBER		
	Theory	Practical	
Microprocessors & Micro controllers	Mrs. A.B. Patil	E1	Mr. Sudhir Bussa
		E2	
		E3	
Electronics Circuit Design	Mr.S.V.Dhole	E1	Mr.S.V.Dhole
		E2	Mr. V.H.Patil
		E3	Mr.S.V.Dhole
Digital Communication	Mr.S. R. Madkar	E1	Mr.S. R. Madkar
		E2	
		E3	
Electromagnetic Engineering	Mr. V.H.Patil	E1	Mr. V.H.Patil
		E2	
		E3	
Audio & Video Engineering	Mr. P.D. Kadam		

**T.T. Incharge  
(Mr.V. H. Patil)**

**H.O.D**

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**TIME TABLE 2011 – 12**

**CLASS: B. Tech (E & TC) Sem: VII  
G.F.M: Mr. Sudhir Bussa**

**w. e. f – 01/08/2011  
Room No.: C-201/208**

TIME	MON	TUE	WED	THURS	FRI	TIME	SAT
10:30	ATD*	ATD^	VD^	ATD^	ATD^	08:30	RRE(\$)
11:30	RRE*	VD^	MDC^	VD^	RRE^	09:30	ADSP (\$)
12:30	RECESS					10:30	
01:15	PROJECT STAGE-I ↑ ↓	E1-ATD E2-ADSP	E1-VD E2-ATD	E1-ADSP E2-VD	MDC (\$)	11:30	RRE*
02:15					ADSP (\$)	12:30	
03:15	SHORT RECESS						
03:30	MDC *	VD*	ATD*	VD*	RRE*		
04:30	ADSP*	MDC*	ADSP*	MDC	ADSP*		

ATD-W509;ADSP-W-406 COMP LAB-1; VD-W-512

\*= C-208; ^ = C-201; \$- W-401

SUBJECT	STAFF MEMBER		
	Theory	Practical	
VLSI Design	Mrs. M.S. Chavan	E1 E2	Mrs. M.S. Chavan
Antenna Theory & Design	Mr. V. P. Kaduskar	E1 E2	Mr. V. P. Kaduskar
Elective – I (ADSP)	Mrs. M.V.Patil	E1 E2	Mr. V. P. Kaduskar
Radio Wave Propagation & RF Engineering			-
Microwave Devices & Circuits	Mr. V. P. Kaduskar		-

**T.T. Incharge  
(Mr. V. H. Patil)**

**H.O.D**