

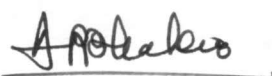
BHARATI VIDYAPEETH UNIVERSITY
COLLEGE OF ENGINEERING, PUNE- 43

Rules and Regulations for the Full Time Ph.D. Candidates Admitted under

Visvesvaraya Ph.D. scheme for Electronics and IT

Academic Year: 2015-16

- 1) Candidate is required to carry out his/her research work regularly under the guidance of the Supervisor(s), without any interruption during the period he/she enjoys the funding.
- 2) Candidate must submit monthly attendance report & progress report duly signed by Research Supervisor & H.O.D. of the department to R&D Cell.
- 3) Leave application of candidate is to be recommended by HOD
- 4) Attendance record of the scholars shall be maintained by concerned supervisor(s) and Head of the Department.
- 5) Maximum leave period for a candidate will be 10 days in a semester excluding public holidays. They will not be entitled to any vacation.
- 6) Candidate must be present during working hours of the institute shall sign in the attendance register on every working day. The Head of the Department shall maintain the attendance register.
- 7) Leave on medical grounds, duly supported by medical certificate (from a registered medical practitioner endorsed by the Medical officer) may be granted to a candidate up to 7 days per semester by HOD on recommendation of guide.
- 8) Candidate will be assigned academic responsibilities up to 10 hours per week. This includes 4 hours theory classes, 6 hours practical, conduct of Seminar/Symposia, running and maintenance of equipment/computer, exam duties etc as may be decided by the Head of the Department.
- 9) No candidate shall be permitted to receive any emoluments, salary, stipend or any other scholarship/assistantship during the tenure of award of Ph.D. degree under Visvesvaraya Ph.D. scheme for Electronics and IT.
- 10) Candidate has to submit undertaking to the respective HOD.


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